

INTEGRATED SEMICONDUCTOR FIN DEVICE AND A METHOD  
FOR MANUFACTURING SUCH DEVICE  
*Collaert et al.*  
Appl. No.: Unknown      Atty Docket: IMEC280.001AUS

Figure 1

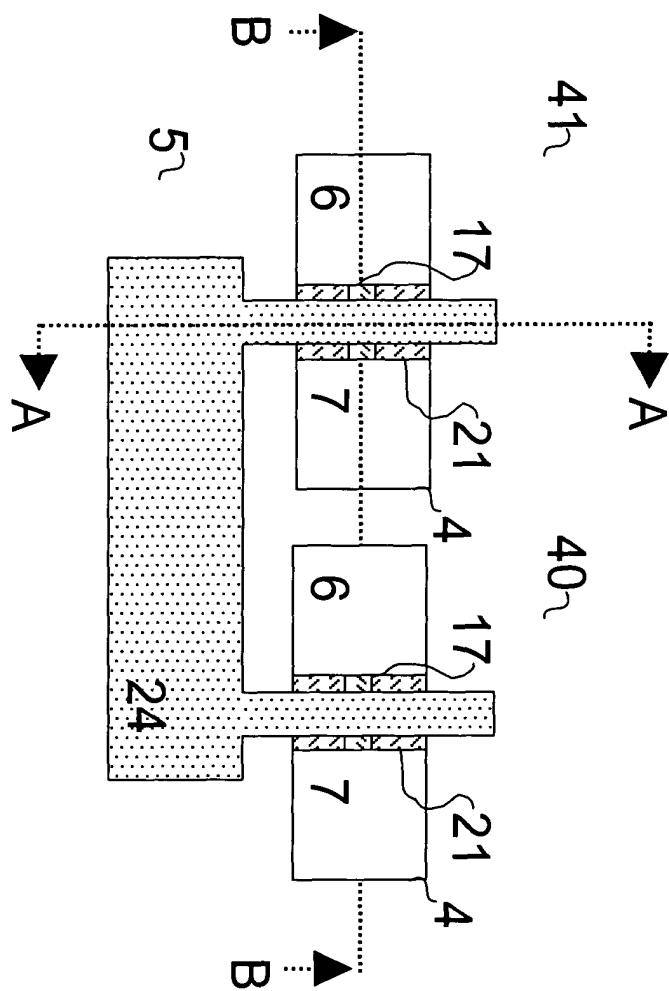


Figure 2a

$\sim 41$

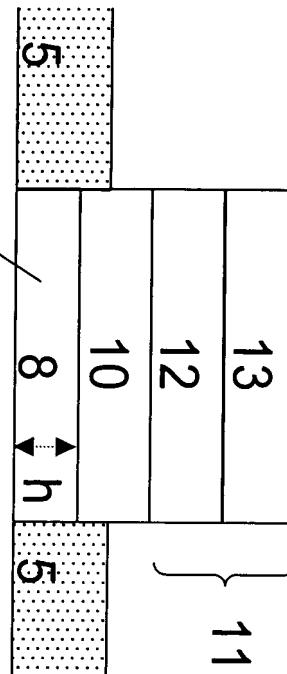
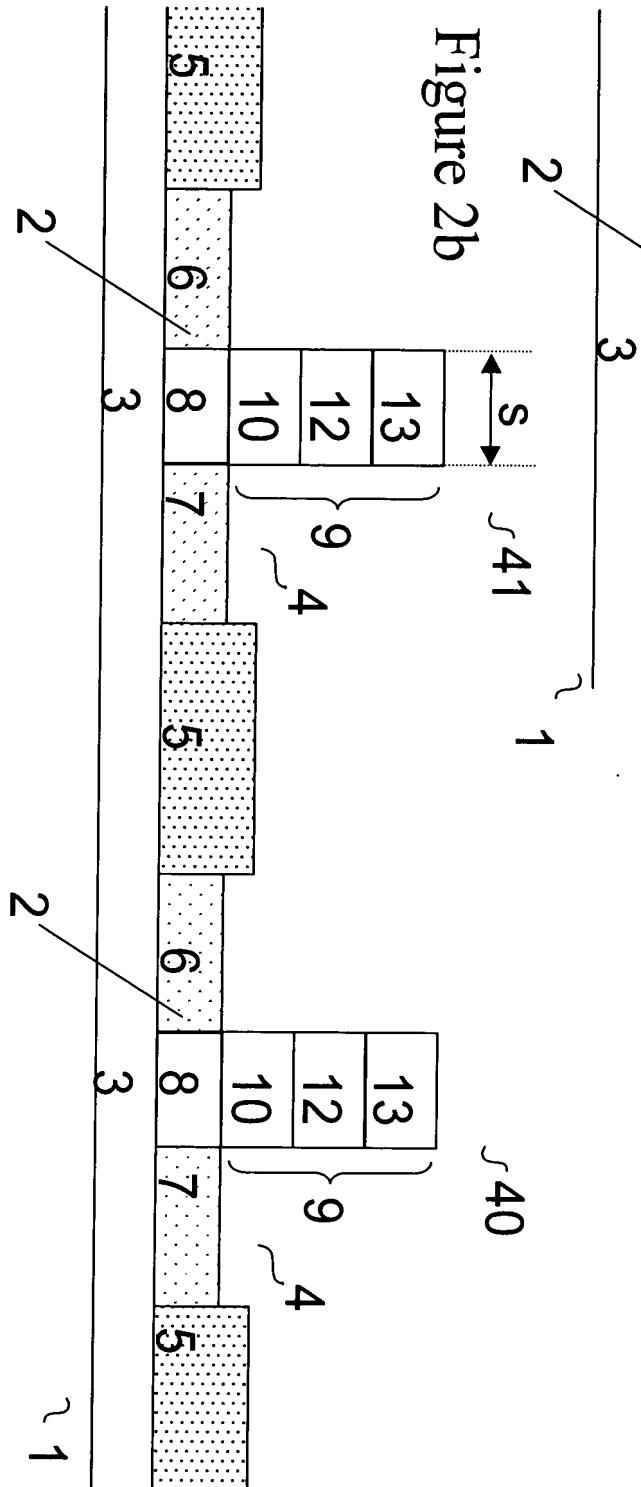


Figure 2b

$\sim 41$

$\sim 1$

$\sim 40$



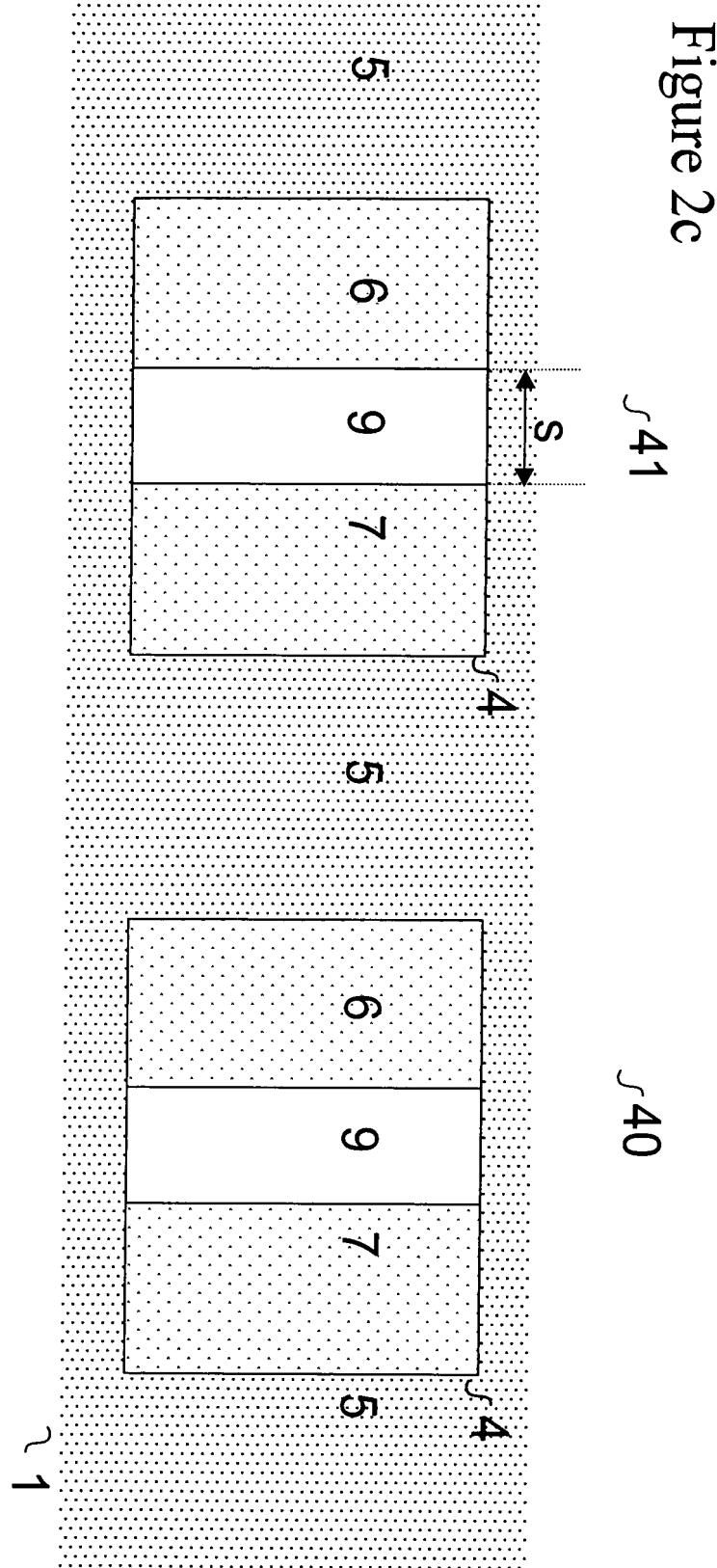


Figure 3a

✓ 41

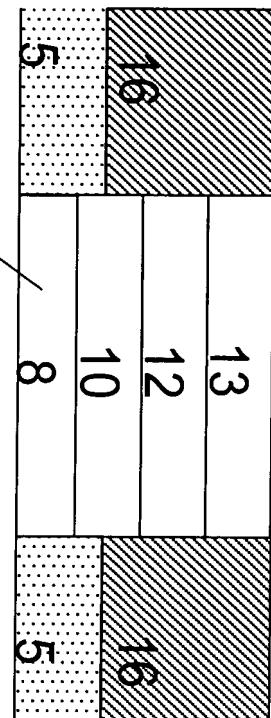
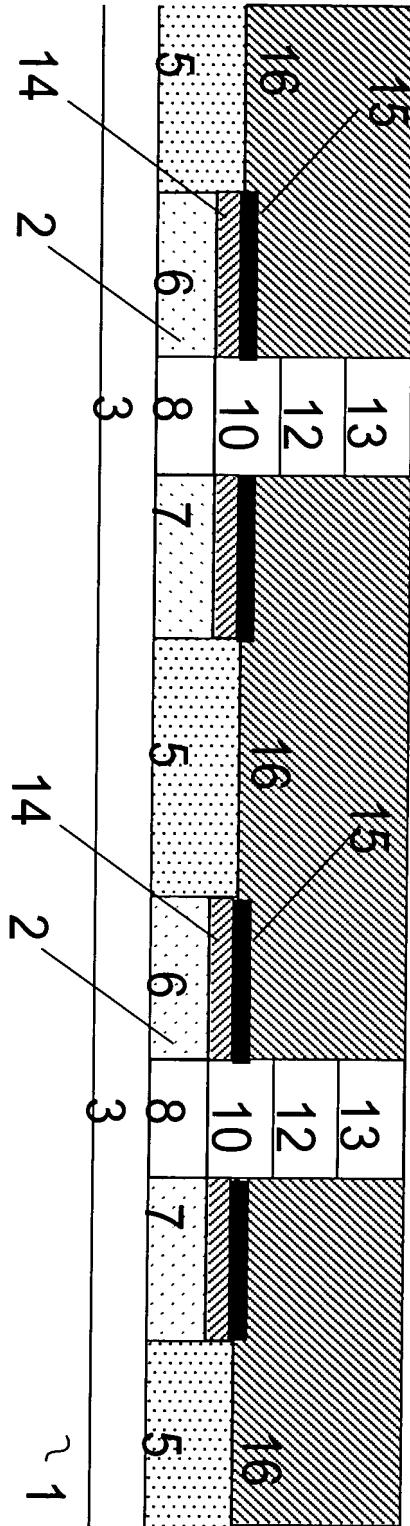


Figure 3b

✓ 41

~ 1

✓ 40



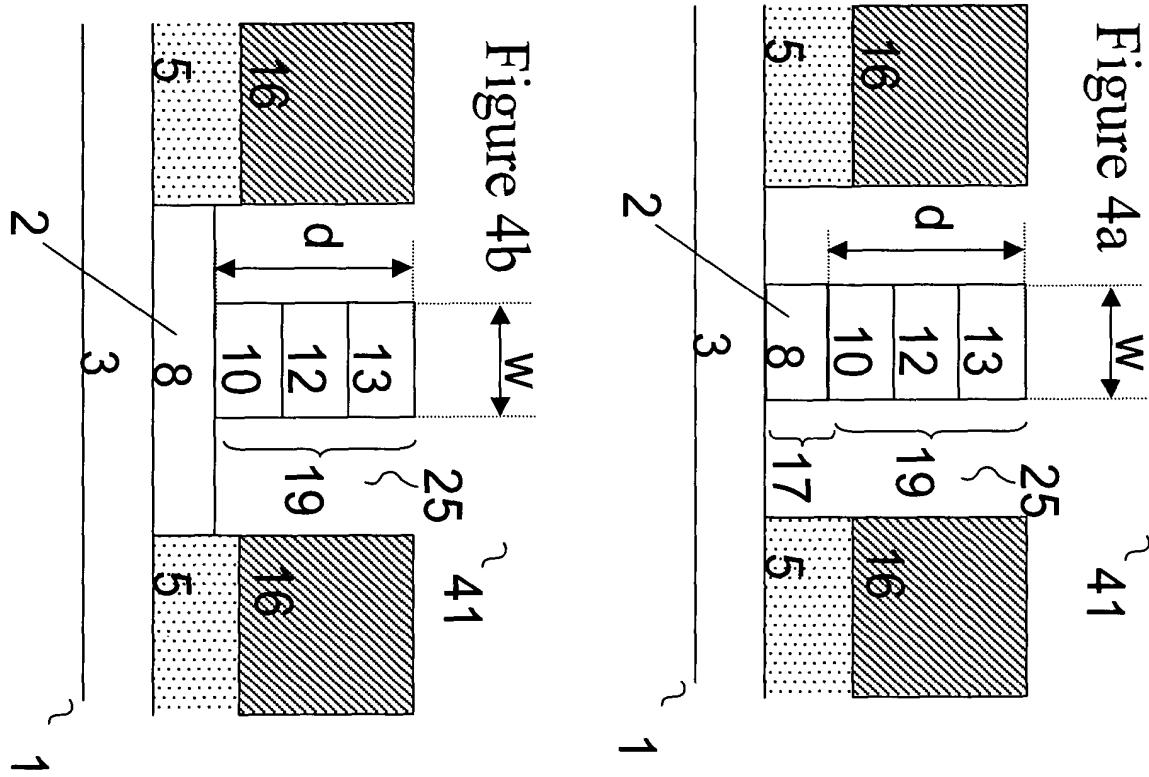


Figure 4c



Figure 4d

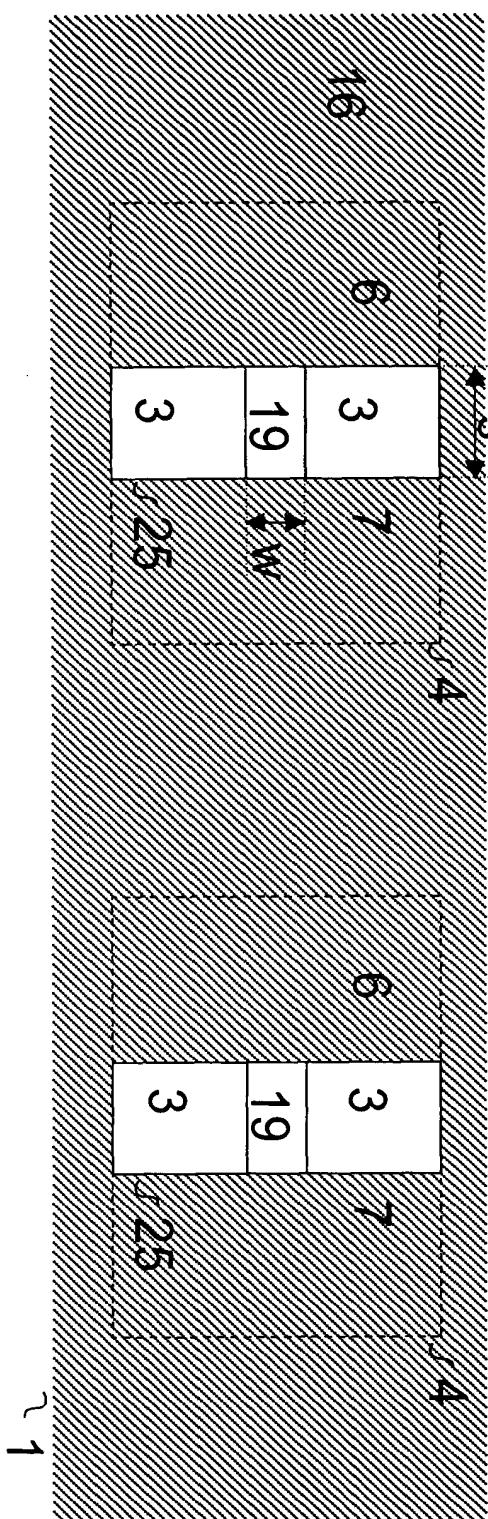


Figure 5a

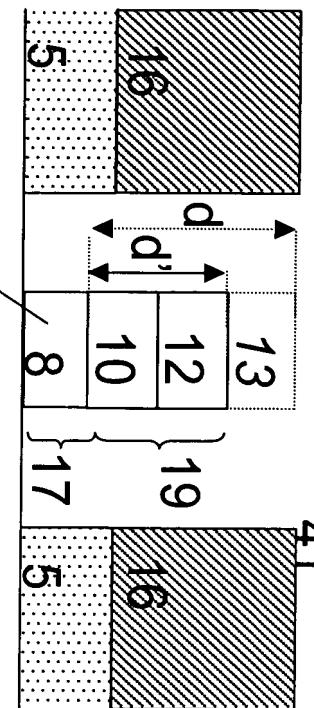
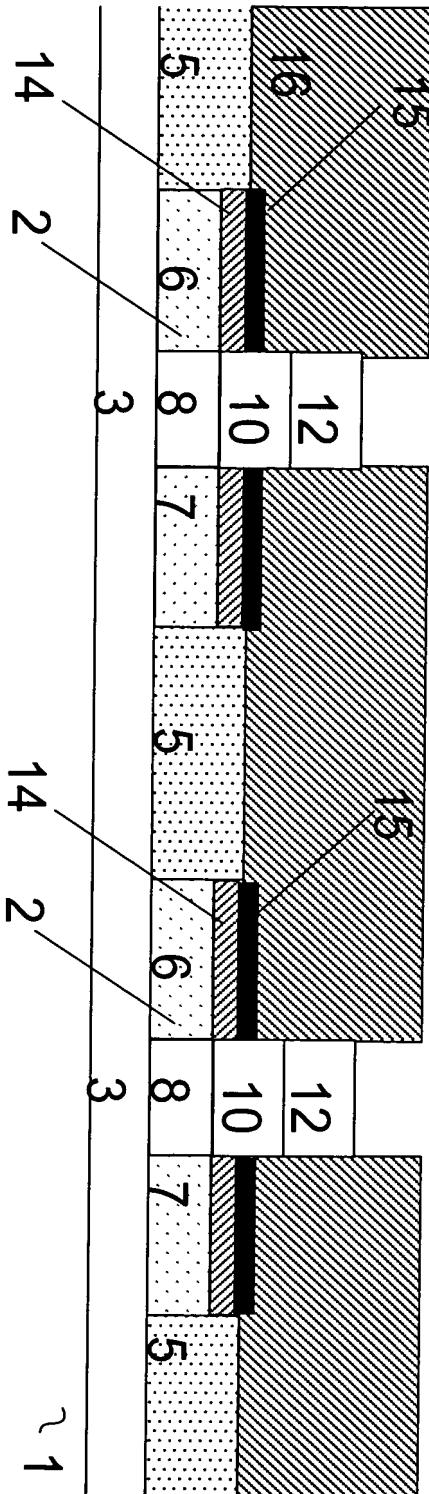


Figure 5b

✓41

2  
3  
1

✓40



~1

Figure 6a

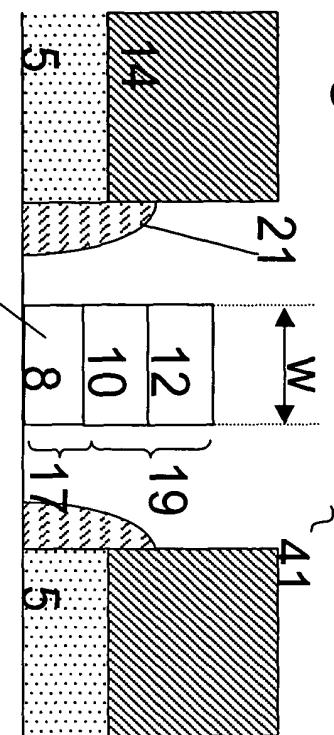
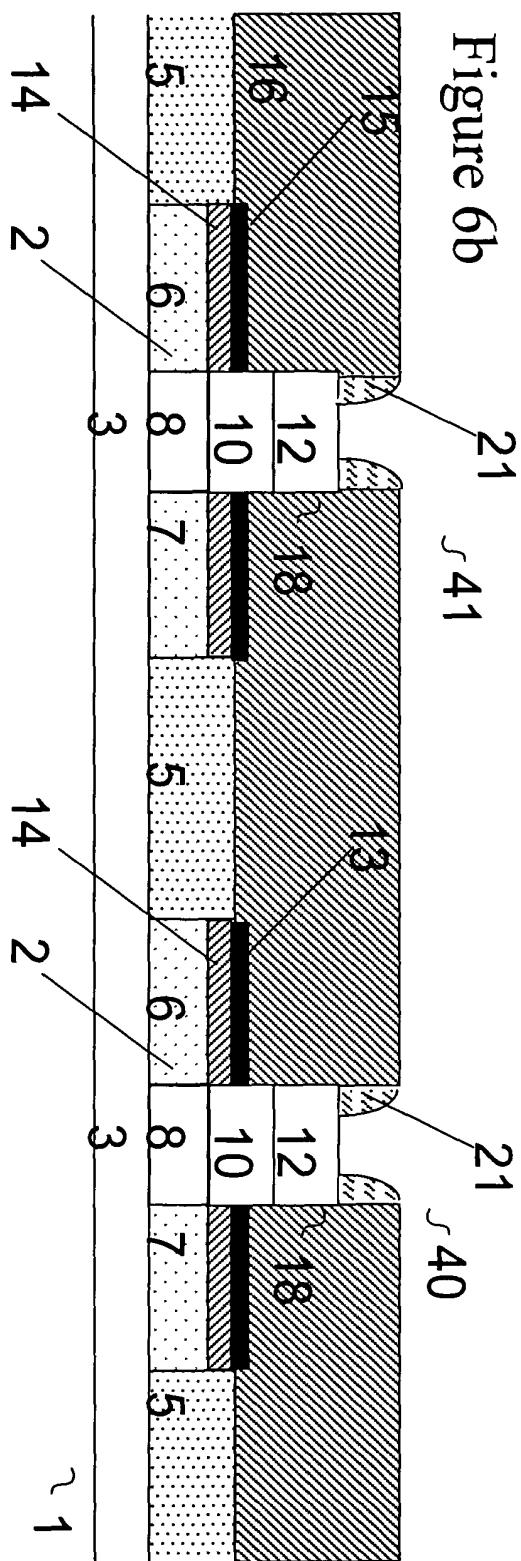
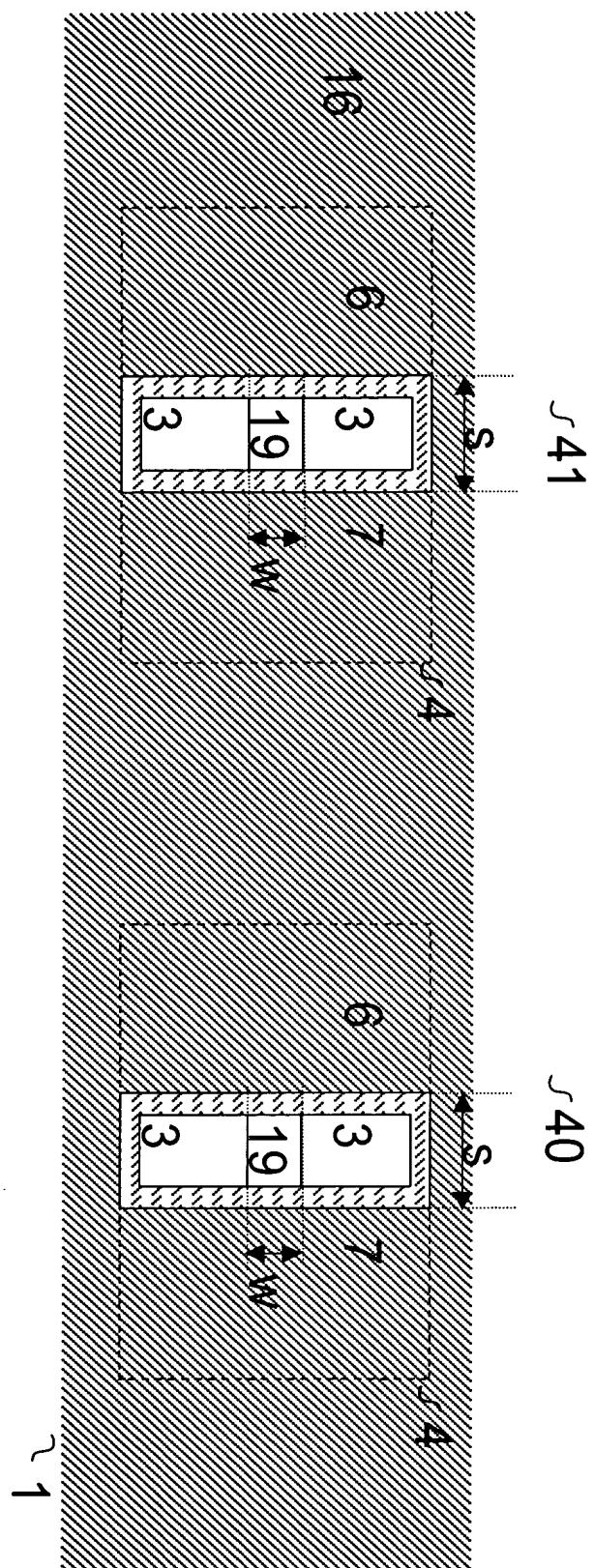


Figure 6b



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Figure 6c



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Figure 7a

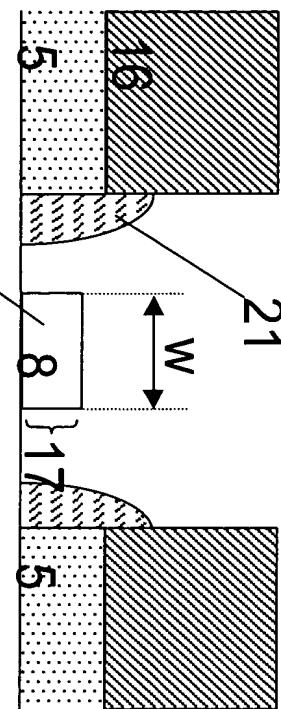


Figure 7b

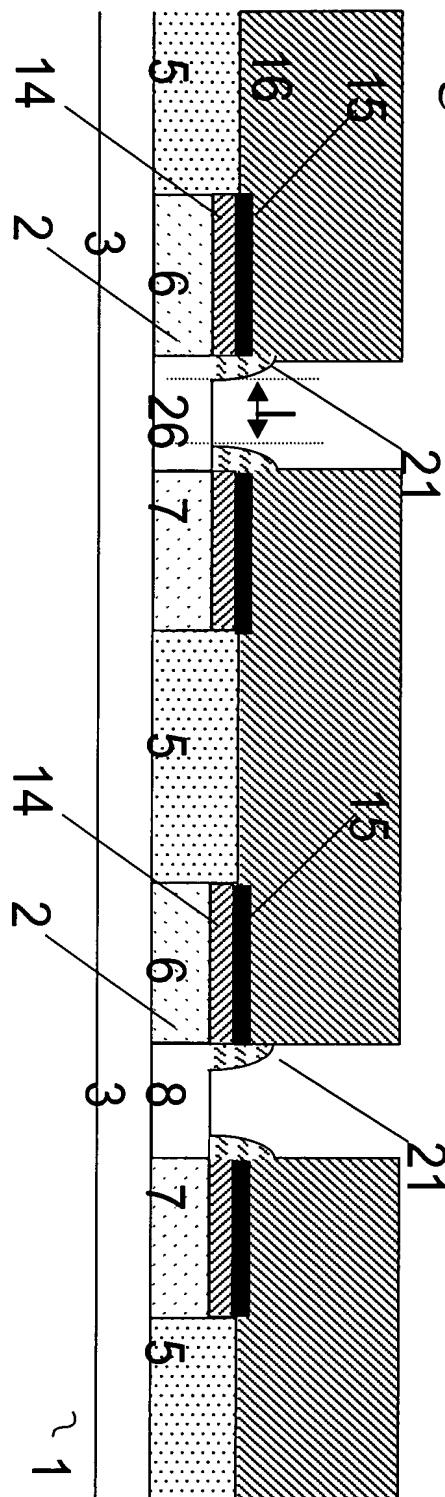


Figure 8a

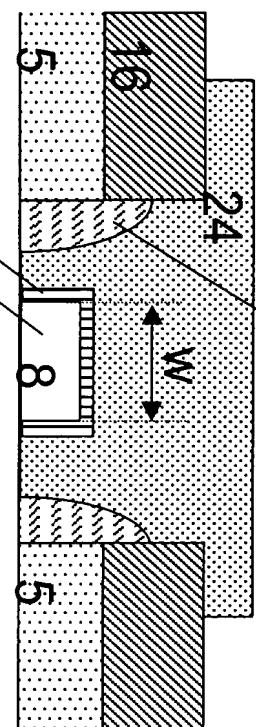
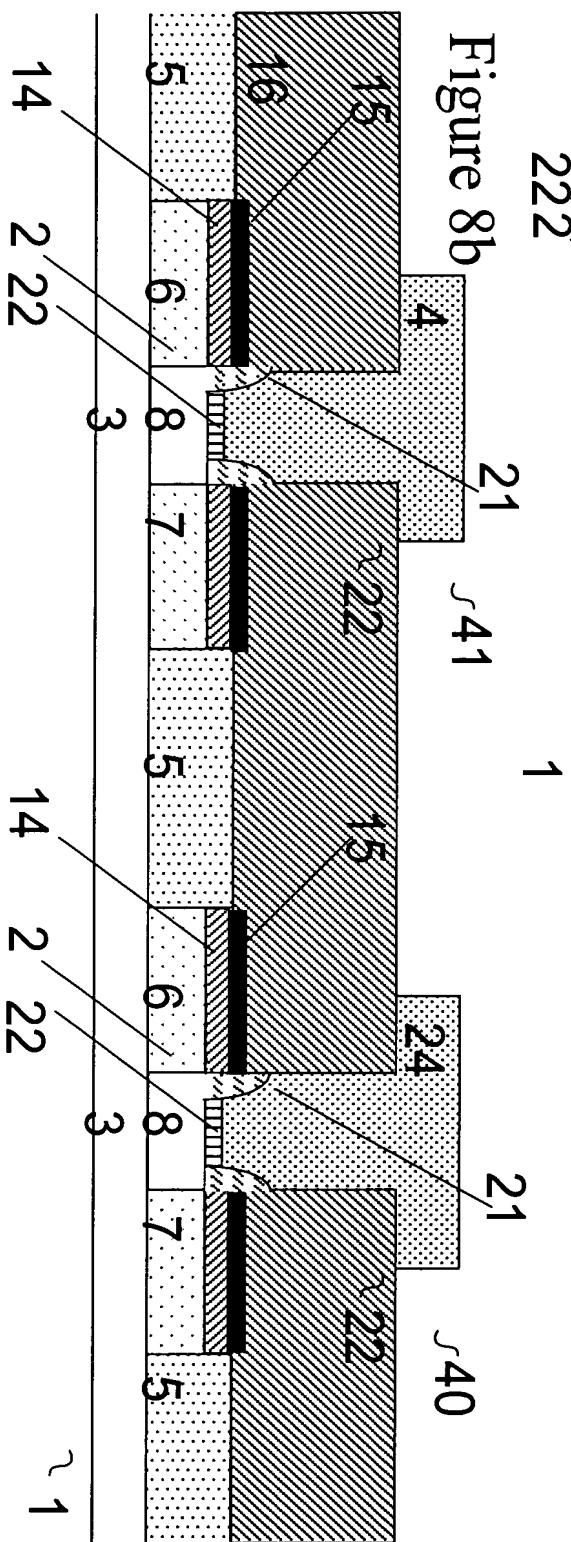


Figure 8b



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Figure 9a

21 ~ 41

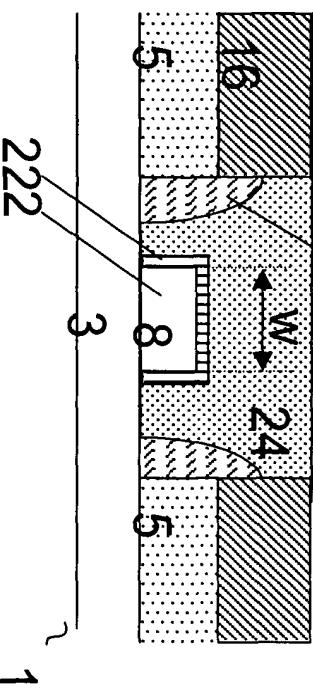
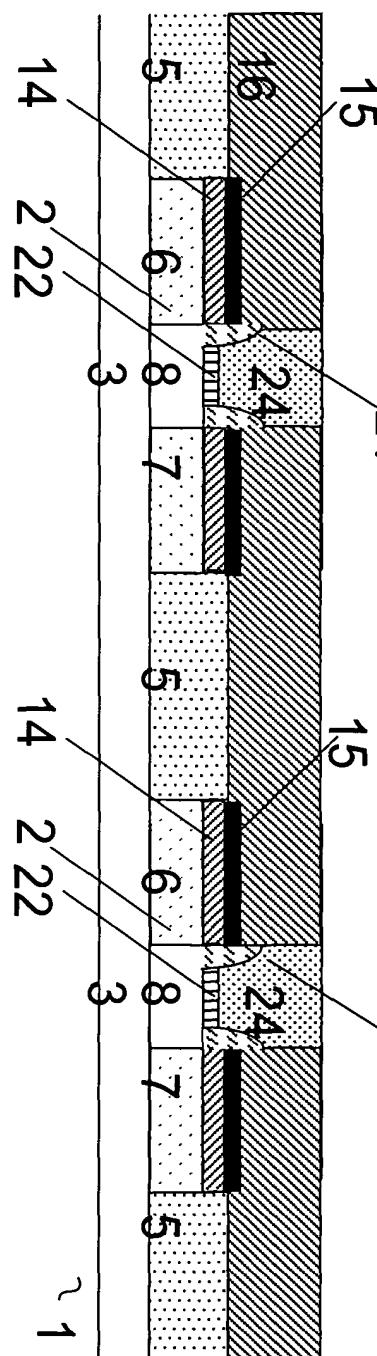


Figure 9b

21 ~ 41

15



21 ~ 40

15

14 2 22

~ 1